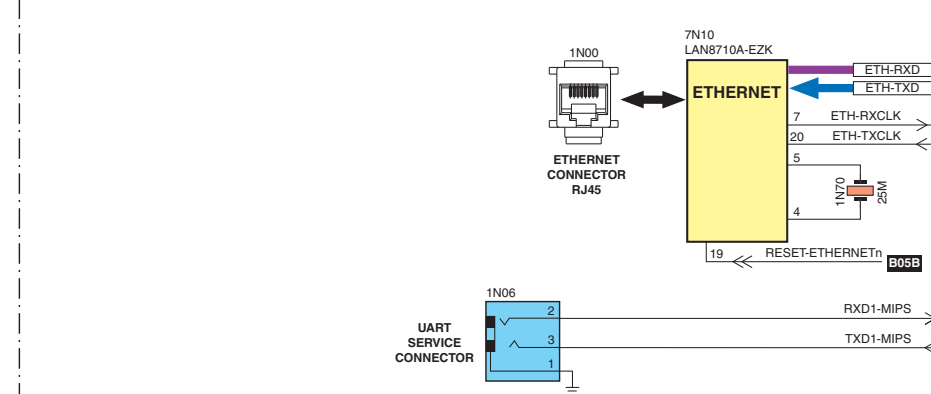


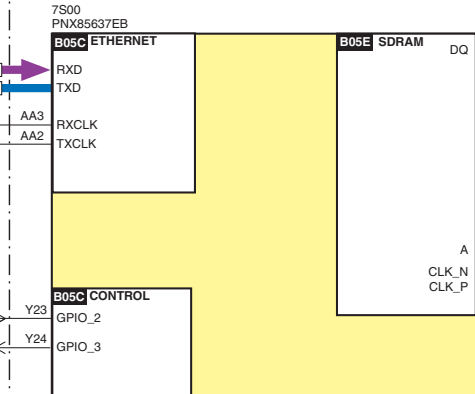
9.7 Block diagram Control + Clock Signals

CONTROL + CLOCK SIGNALS

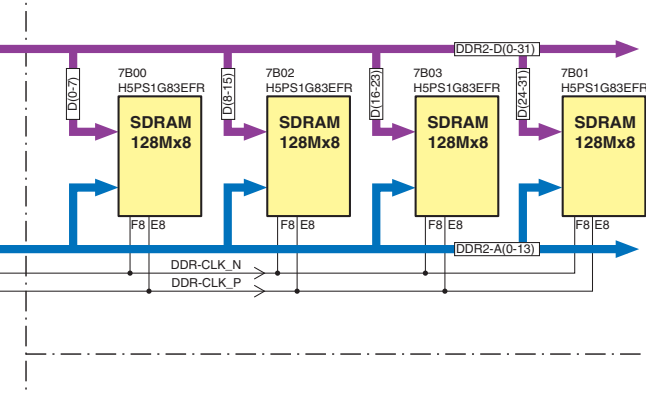
B06C ETHERNET + SERVICE



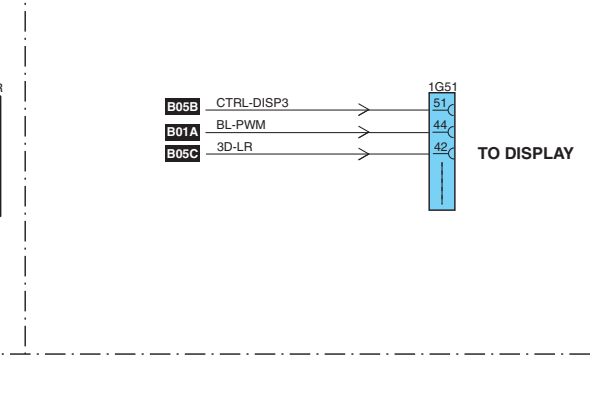
B05x PNX85500



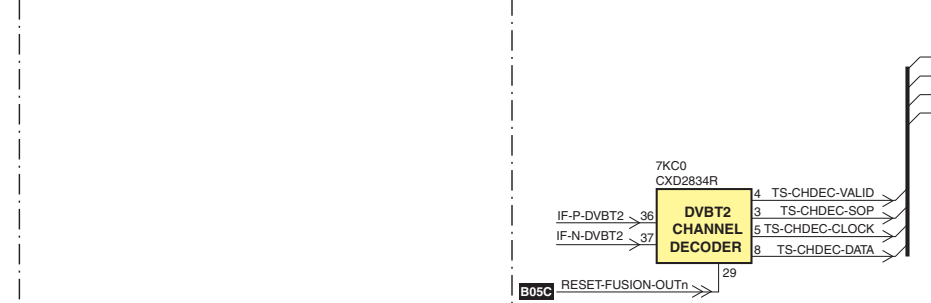
B04A DDR



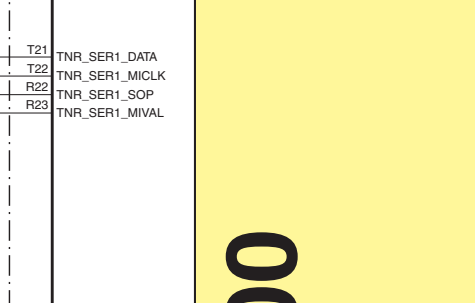
B09A VIDEO OUT - LVDS



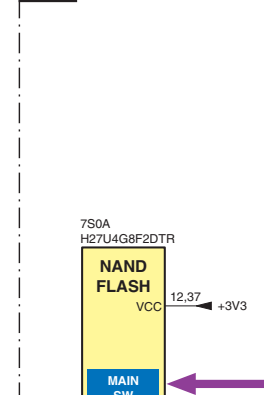
B08A DVBT2



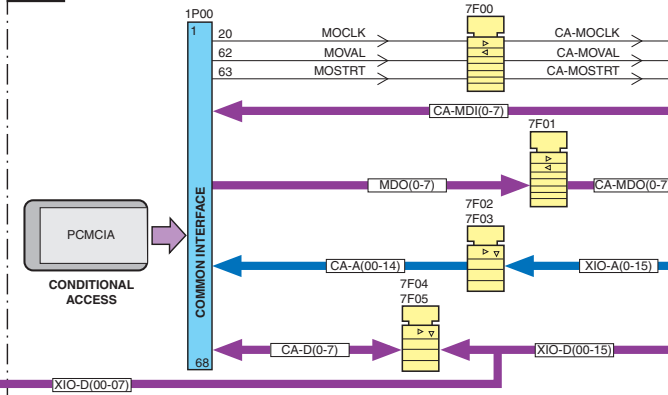
B05F VIDEO STREAM



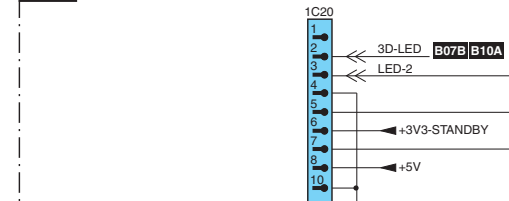
B05F NANDFLASH



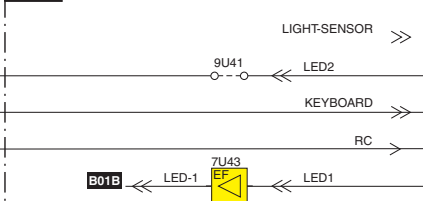
B05G COMMON INTERFACE



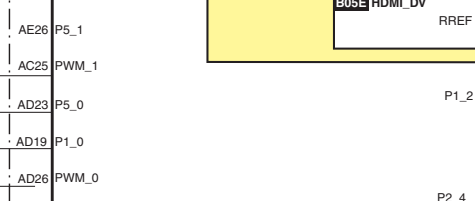
B01B INTERFACE CONNECTORS



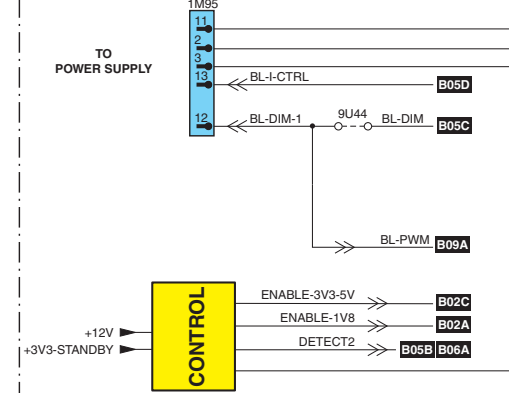
B01A POWER CONNECTORS



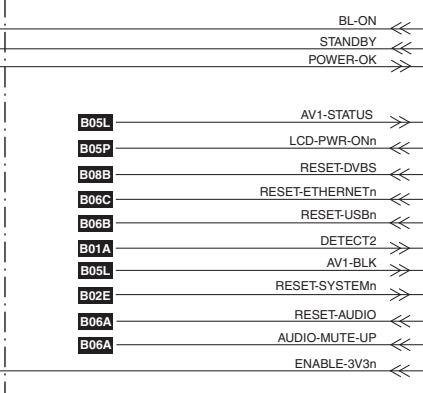
B05B STANDBY



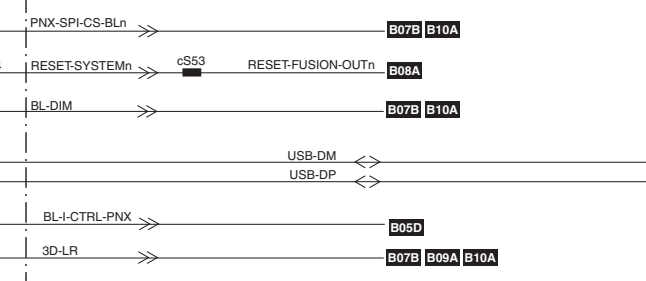
B01A POWER CONNECTORS



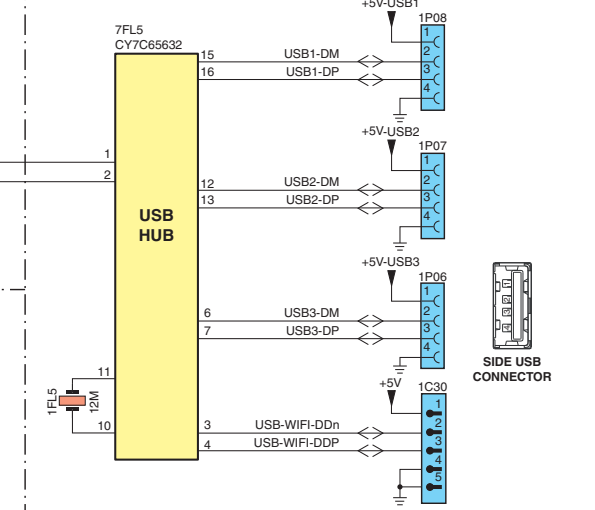
B05B PNX85500: STANDBY CONTROLLER



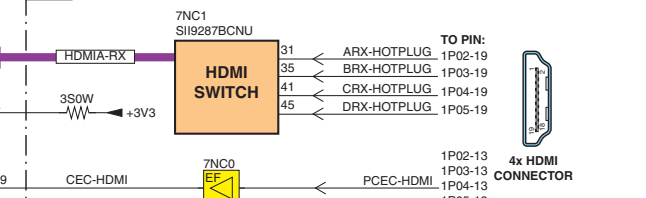
B05C PNX85500: MIPS



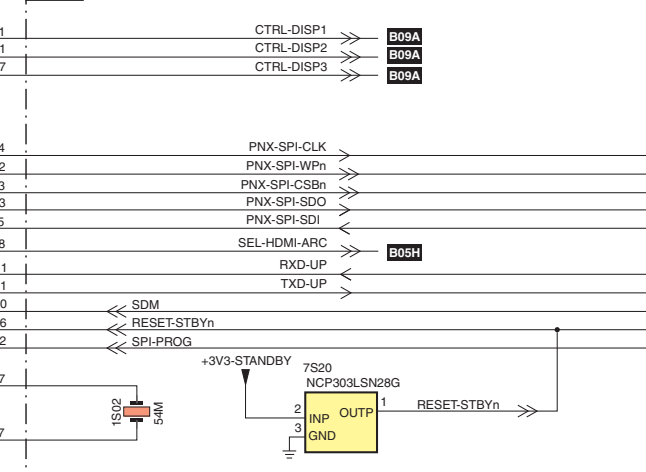
B06B USB HUB



B06D HDMI



B05B PNX85500: STANDBY CONTROLLER



B05D PNX85500: CONTROL

