


AD810 AD811 AD812

NPN DUAL MONOLITHIC SILICON NITROX® TRANSISTORS

AD813
AD818
MONOLITHIC MATCHED PAIRS FOR DIFFERENTIAL AMPLIFIERS
HIGH GAIN $h_{FE} \geq 150$ @ $10\mu A - 1mA$
LOG CONFORMANCE AD818 $\Delta r_e \leq 1\Omega$ from ideal TYP.

 V_{BE} MATCHING .. $|V_{BE1} - V_{BE2}| = .4mV$ TYP.

HIGH f_T 250 MHz TYP. @ 1mA

ABSOLUTE MAXIMUM RATINGS (Note 1)
@ 25°C (unless otherwise noted)

Maximum Temperatures

Storage Temperature -65° to +200°C
Operating Junction Temperature +150°C
Lead Temperature (Soldering, 10 second time limit) +260°C

Maximum Power Dissipation
ONE SIDE BOTH SIDES

Device Dissipation @ Free Air 250mW 500mW

Linear Derating Factor 2.3mW/°C 4.3mW/°C

Maximum Voltage and Current for Each Transistor

	AD810	AD811	AD813	AD812	AD818
V_{CBO} Collector to Base Voltage	25V	45V	60V	45V	25V
V_{CEO} Collector to Emitter Voltage	25V	45V	60V	45V	25V
V_{EBO} Emitter to Base Voltage (note 2)	6.5V	6.5V	6.5V	6.5V	6.5V
V_{CCO} Collector to Collector Voltage	35V	40V	50V	50V	45V
I_C Collector Current	20mA	20mA	20mA	20mA	20mA

ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTICS	AD810	AD811	AD813	AD812	AD818	UNITS	CONDITIONS
Δr_e	Log Conformance					1.0 0.2 MAX.	$\frac{\Omega}{\Omega}$	$I_C = 10-100-1000\mu A$, $V_{CE} = 5V$
h_{FE}	DC Current Gain	100	200	200	400	150 MIN.		$I_C = 10\mu A$, $V_{CE} = 5V$
h_{FE}	DC Current Gain	100	200	200	400	150 MIN.		$I_C = 1mA$, $V_{CE} = 5V$
h_{FE}	DC Current Gain	85	170	170	350	150 MIN.		$I_C = 5mA$, $V_{CE} = 5V$
$h_{FE} (-55^\circ C)$	DC Current Gain	35	75	75	150	50 MIN.		$I_C = 10\mu A$, $V_{CE} = 5V$
$V_{BE} (ON)$	Emitter-Base "ON" Voltage	0.7	0.7	0.7	0.7	0.7 MAX.	V	$I_C = 10\mu A$, $V_{CE} = 5V$
	Collector Saturation Voltage	0.5		0.3	0.3		V	$I_C = 1mA$, $I_B = 0.1mA$
$V_{CE} (SAT)$	Collector Saturation Voltage	1.0	0.8	0.8	0.8		V	$I_C = 5mA$, $I_B = 0.5mA$
I_{CBO}	Collector Cutoff Current	1.0	0.2	0.2	0.2	0.2 MAX.	nA	$I_E = 0$, $V_{CB} = \text{Note 3}$
$I_{CBO} (+150^\circ C)$	Collector Cutoff Current	1.0	0.2	0.2	0.2	0.2 MAX.	μA	$I_E = 0$, $V_{CB} = \text{Note 3}$
I_{EBO}	Emitter Cutoff Current	0.3	0.2	0.2	0.2	0.2 MAX.	nA	$I_C = 0$, $V_{EB} = 5V$
C_{OBO}	Output Capacitance	2.5	2.5	2.5	2.5	2.5 MAX.	pF	$I_E = 0$, $V_{CB} = 5V$
C_{TE}	Emitter Transition Capacitance	3	3	3	3	2 MAX.	pF	$I_C = 0$, $V_{EB} = 0.5V$
C_{C1C2}	Collector to Collector Capacitance	3	3	3	3	2 MAX.	pF	$V_{CC} = 0$

Notes and Additional Electrical Characteristics on next page.

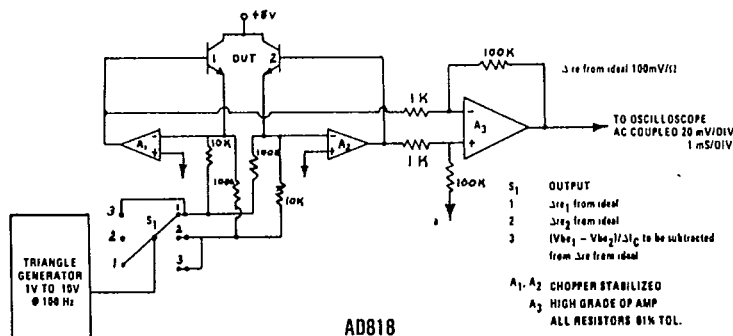
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AD810 811 812 813 818**ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)**

SYMBOL	CHARACTERISTICS	AD810	AD811	AD813	AD812	AD818			CONDITIONS
I_{C1C2}	Collector to Collector Leakage Current	1.0	0.5	0.5	0.5	0.5	MAX.	nA	$V_{CE} = \text{Note 4}$
f_T	Current Gain Bandwidth Product	100	100	100	100	100	MIN.	MHz	$I_C = 200\mu A, V_{CE} = 5V$
f_T	Current Gain Bandwidth Product	125	125	125	125	200	MIN.	MHz	$I_C = 1mA, V_{CE} = 5V$
Noise	Broad Band RMS Noise	1.0	1.0	1.0	1.0	1.0	MAX.	dB	$I_C = 100\mu A, V_{CE} = 5V$ $BW = 200 \text{ Hz}, R_G = 10 \text{ Kohms}$
BV_{C80}	Collector-Base Breakdown Voltage	35	45	45	35	25	MIN.	V	$I_C = 10\mu A, I_E = 0$
BV_{E80}	Emitter-Base Breakdown Voltage	6.5	6.5	6.5	6.5	6.5	MIN.	V	$I_E = 10\mu A, I_C = 0$
$V_{CE0} (\text{SUST})$	Collector-Emitter Sustaining Voltage	35	45	45	35	25	MIN.	V	$I_B = 0, I_C = 100\mu A$

MATCHING CHARACTERISTICS = 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTICS	AD810	AD811	AD813	AD812	AD818			CONDITIONS
$ V_{BE1} - V_{BE2} $	Base Emitter Voltage Differential	1.0	0.4	0.2	0.4	0.4	TYP.	mV	$I_C = 10\mu A, V_{CE} = 5V$
		3.0	1.5	0.5	1.0	1.0	MAX.	mV	
$\Delta V_{BE1} - V_{BE2} / ^\circ C$	Base Emitter Voltage Differential Change with Temperature	2.0	1	.5	1	1	TYP.	$\mu V / ^\circ C$	$I_C = 10\mu A, V_{CE} = 5V$
		15	7.5	2.5	5.0	5.0	MAX.	$\mu V / ^\circ C$	$T_A = -55^\circ C \text{ to } +125^\circ C$
$ I_{B1} - I_{B2} $	Base Current Differential	20	10	5	2.5	10	MAX.	nA	$I_C = 10\mu A, V_{CE} = 5V$
$ \Delta(I_{B1} - I_{B2}) / ^\circ C$	Base Current Differential Change With Temperature	0.6	0.3	0.3	0.5	0.5	MAX.	$nA / ^\circ C$	$I_C = 10\mu A, V_{CE} = 5V$ $T_A = -55^\circ C \text{ to } +125^\circ C$
h_{FE1} / h_{FE2}	DC Current Gain Differential	10	5	5	5	5	TYP.	%	$I_C = 10\mu A, V_{CE} = 5V$

5**LOG CONFORMANCE TEST CIRCUIT****NOTES:**

- These ratings are limiting values above which the serviceability of any semiconductor may be impaired.
- The reverse base-to-emitter voltage must never exceed 7.0 volts and the reverse base-to-emitter current must never exceed 10μAmps.
- For AD810 & AD818 $V_{CB} = 20V$; for AD811, AD812 & AD813 $V_{CB} = 30V$
- For AD810, AD811, AD812 & AD818 $V_{CE} = \pm 45V$; for AD813 $V_{CE} = \pm 100V$

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