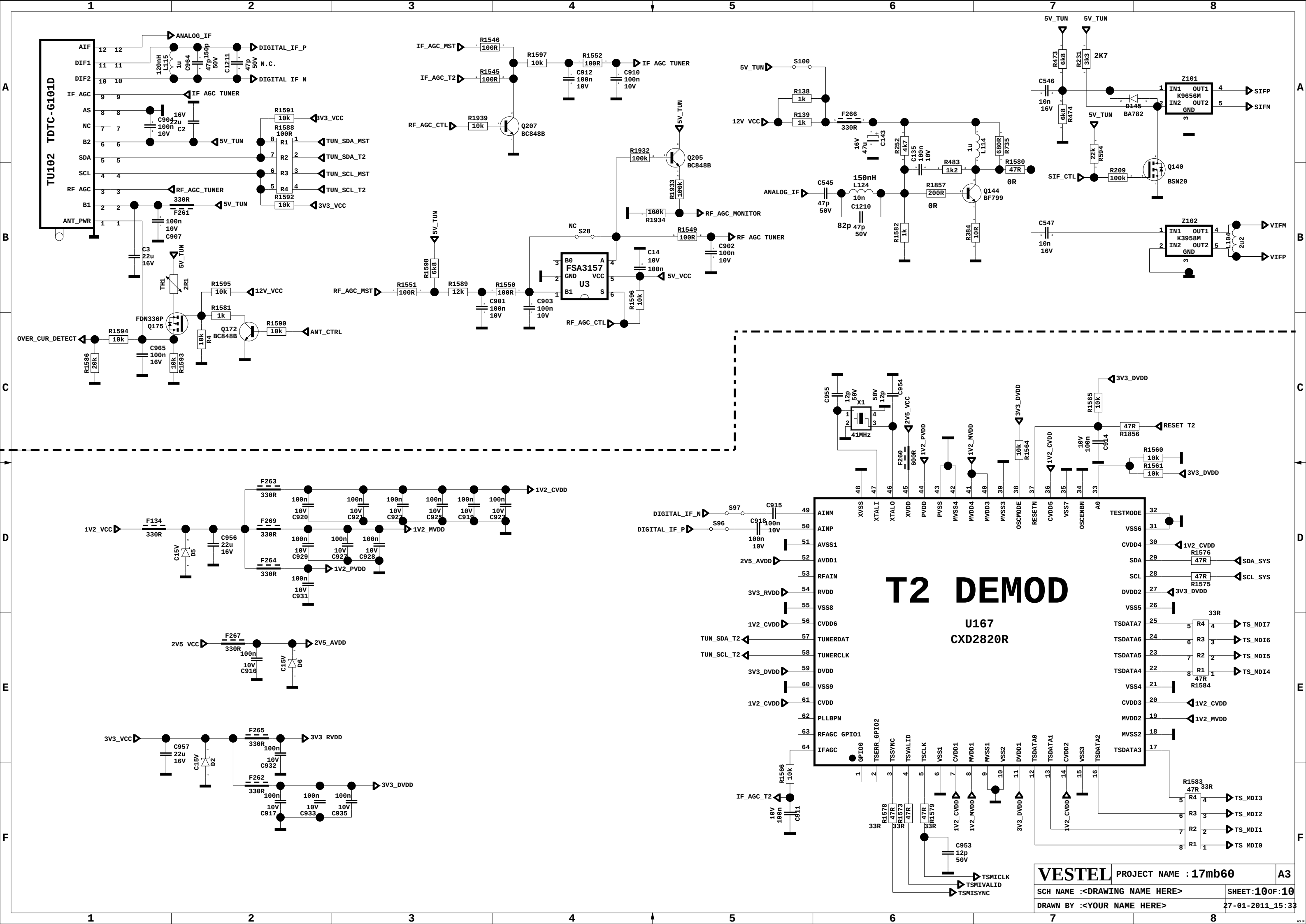
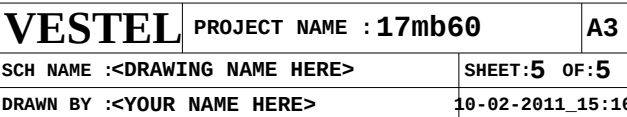
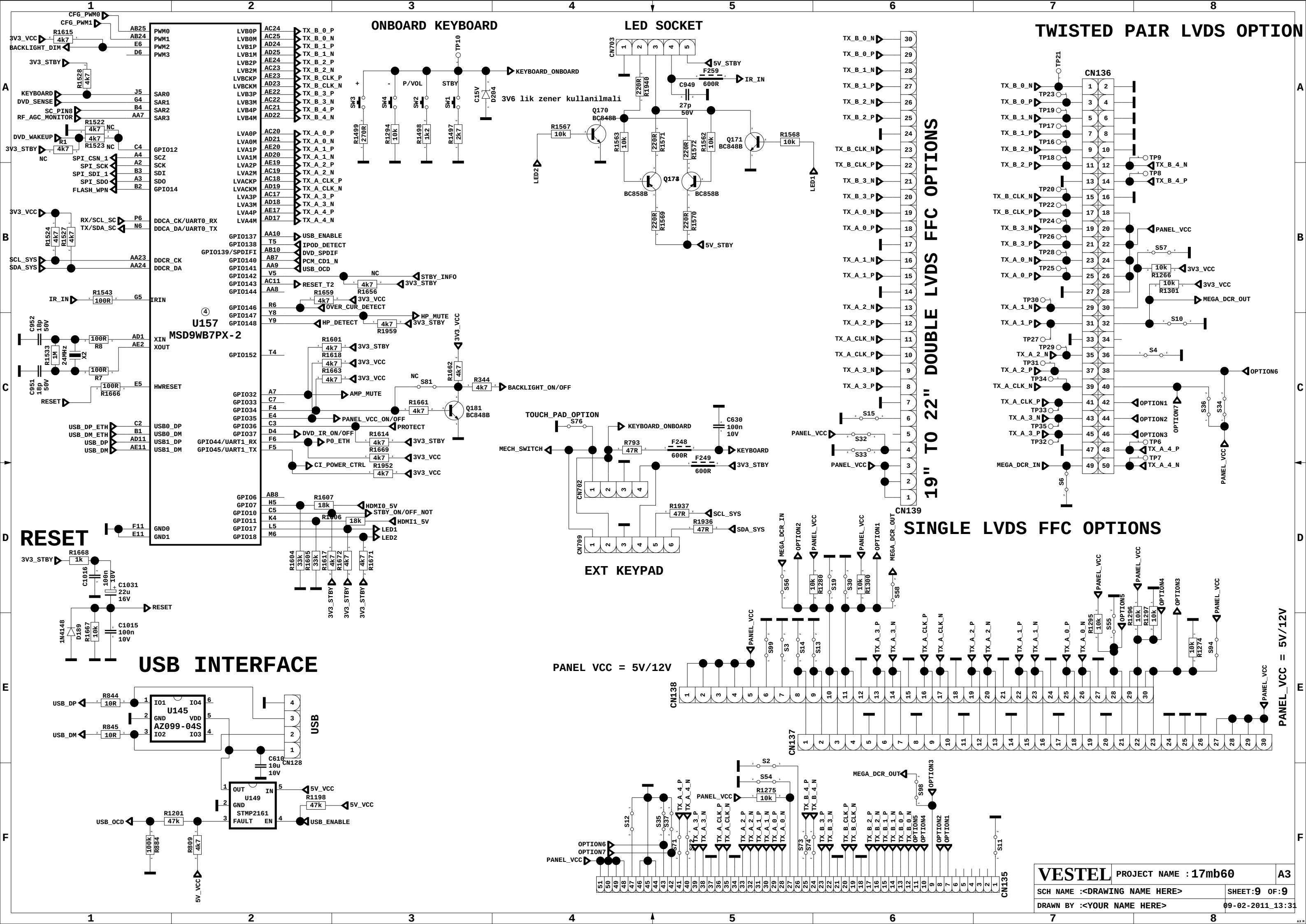
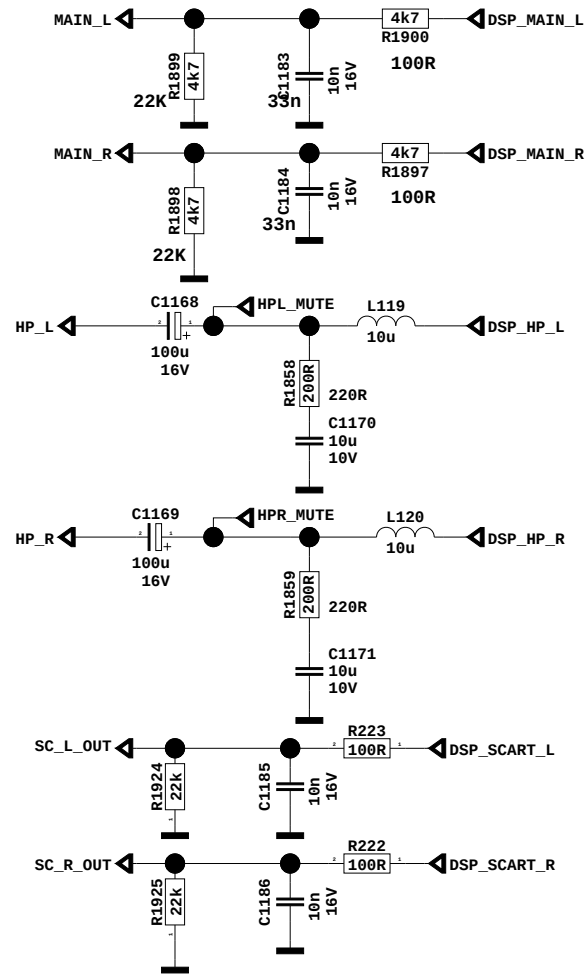




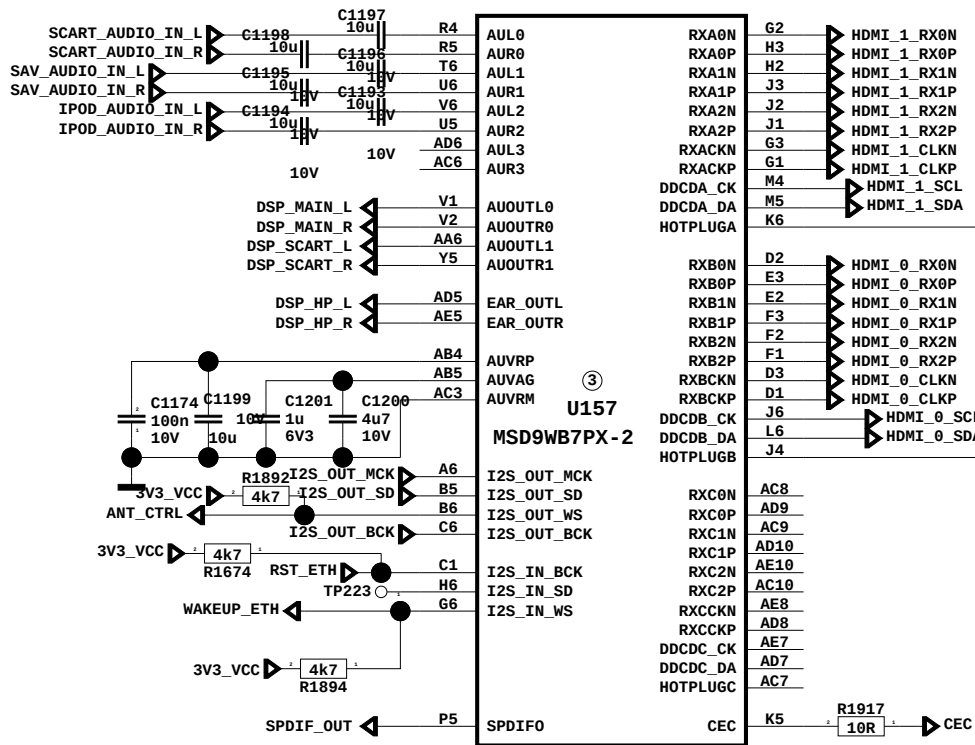
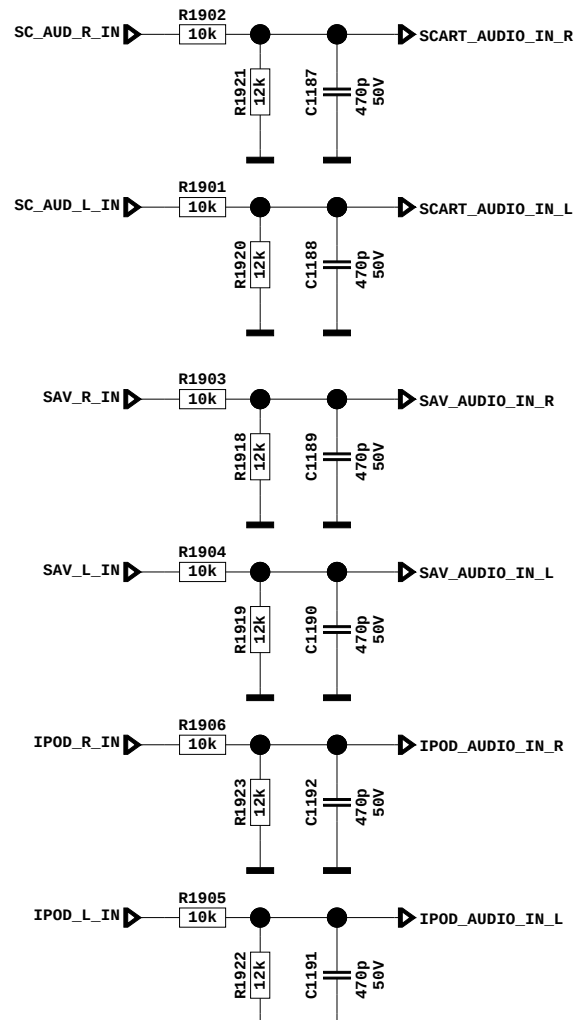




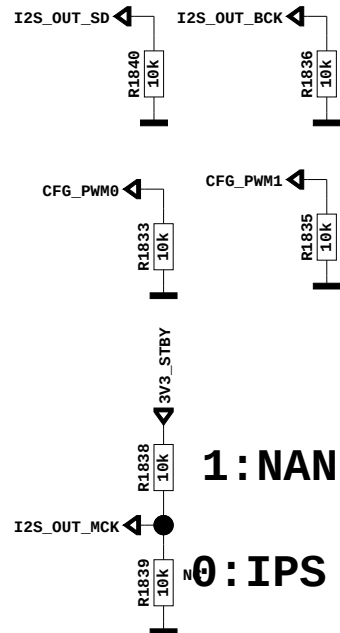
AUDIO OUTPUTs



AUDIO INPUTs



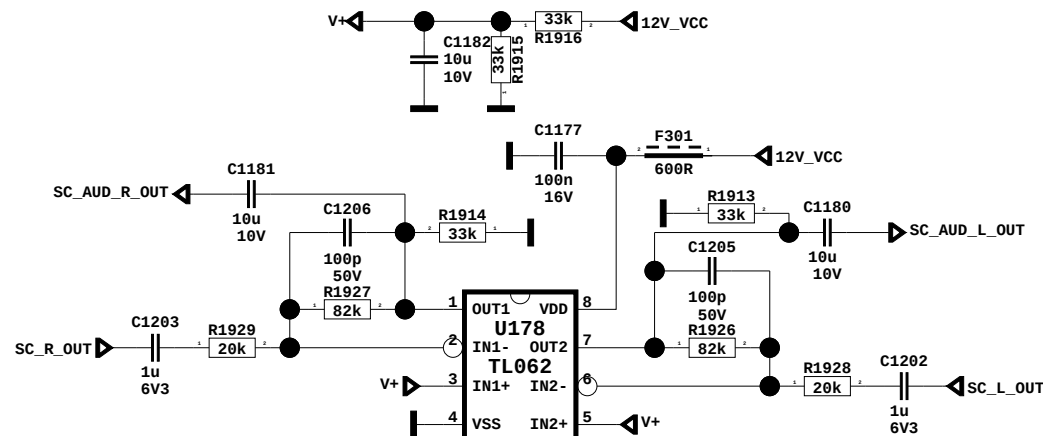
MODE SELECTION



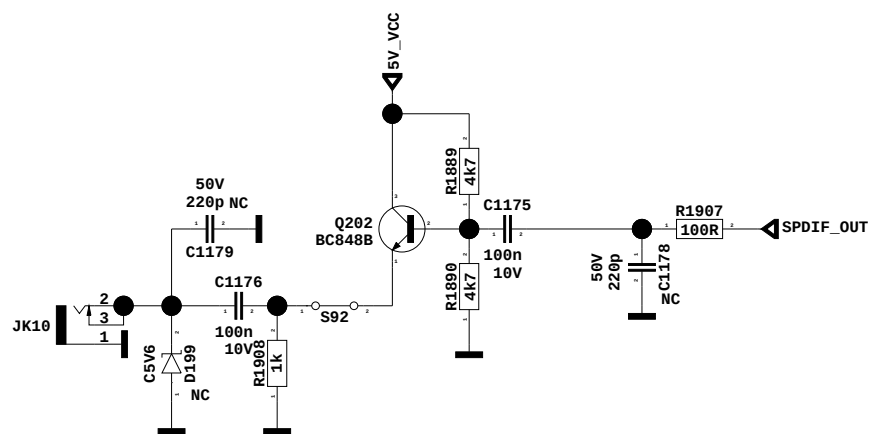
1: NAND

0 : IPS

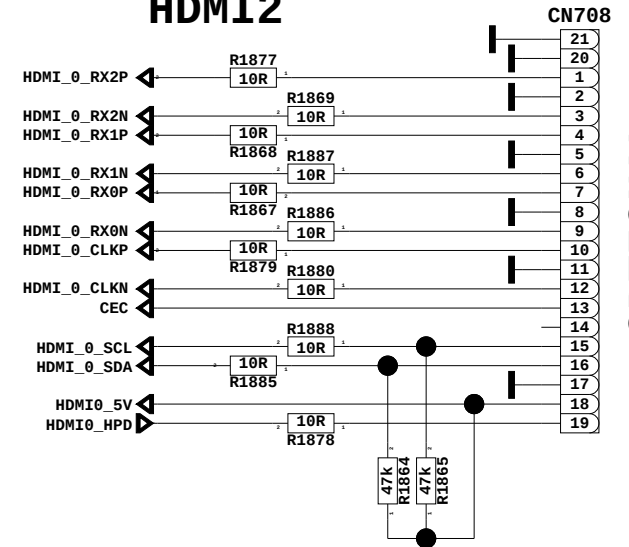
PRE-AMP for SCART AUDIO



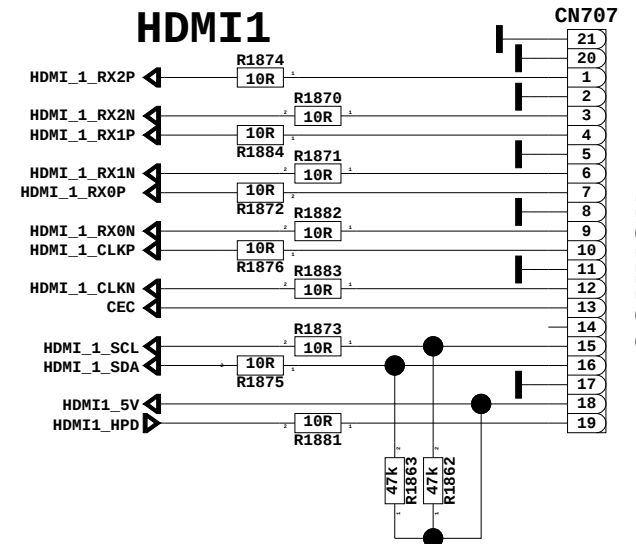
COAX SPDIF OUT



HDMI2



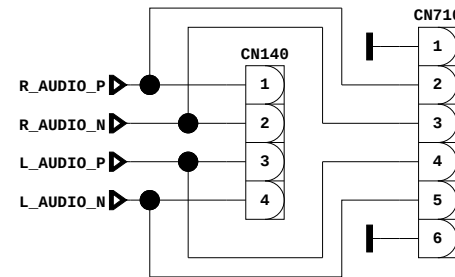
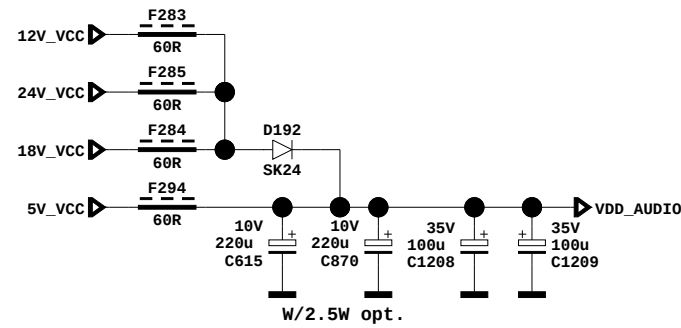
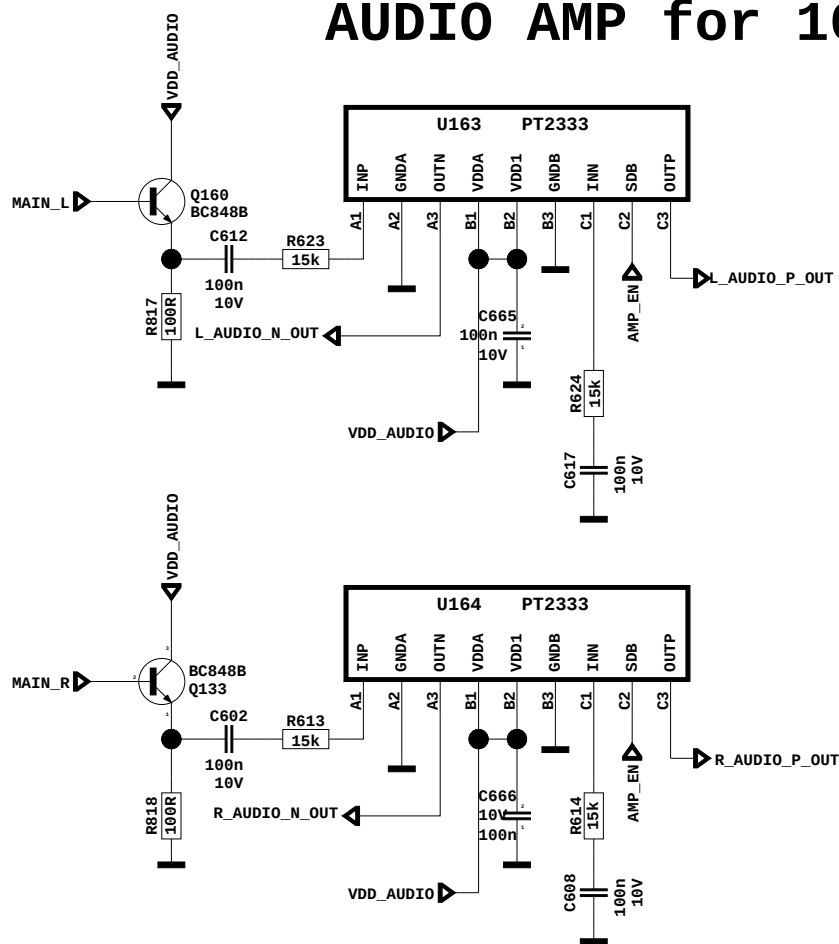
HDMI1



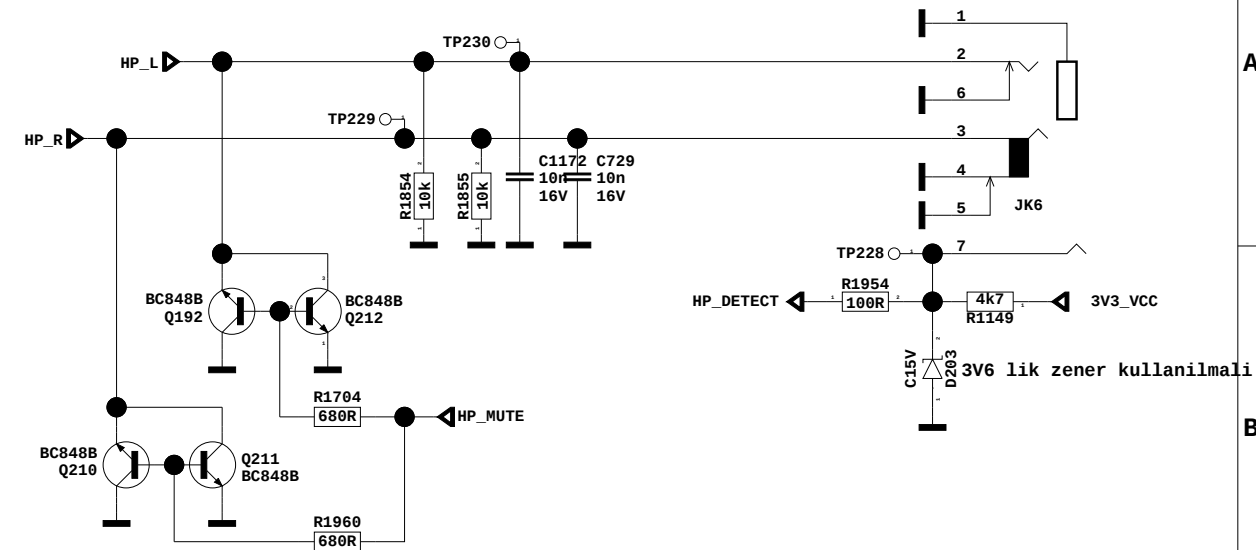
OPTIONAL

COMMON

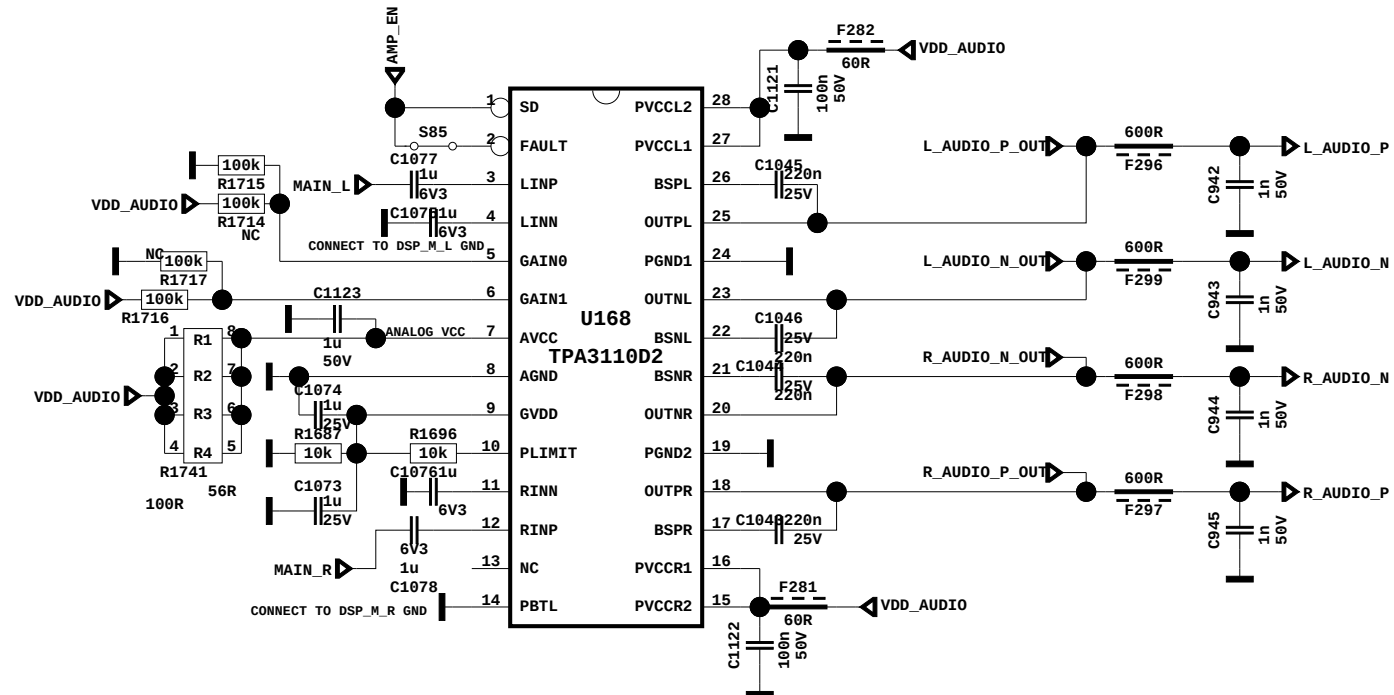
AUDIO AMP for 16" to 24"



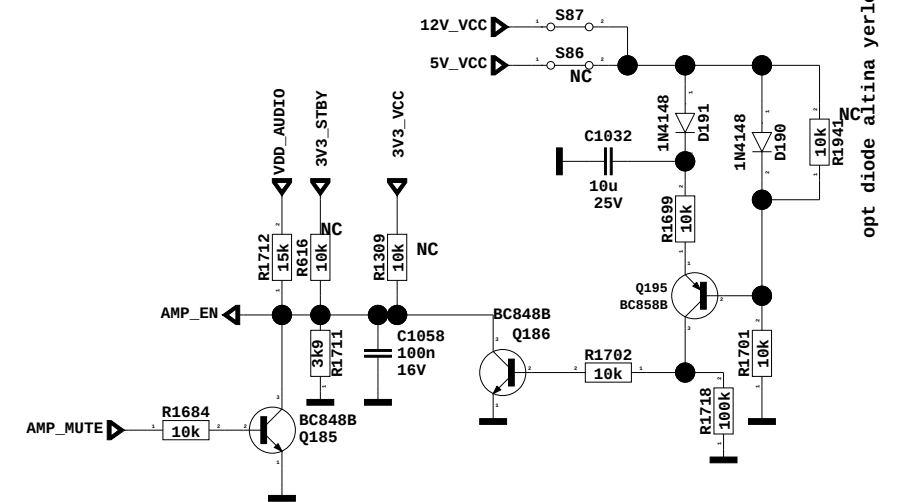
HEADPHONE OUTPUT

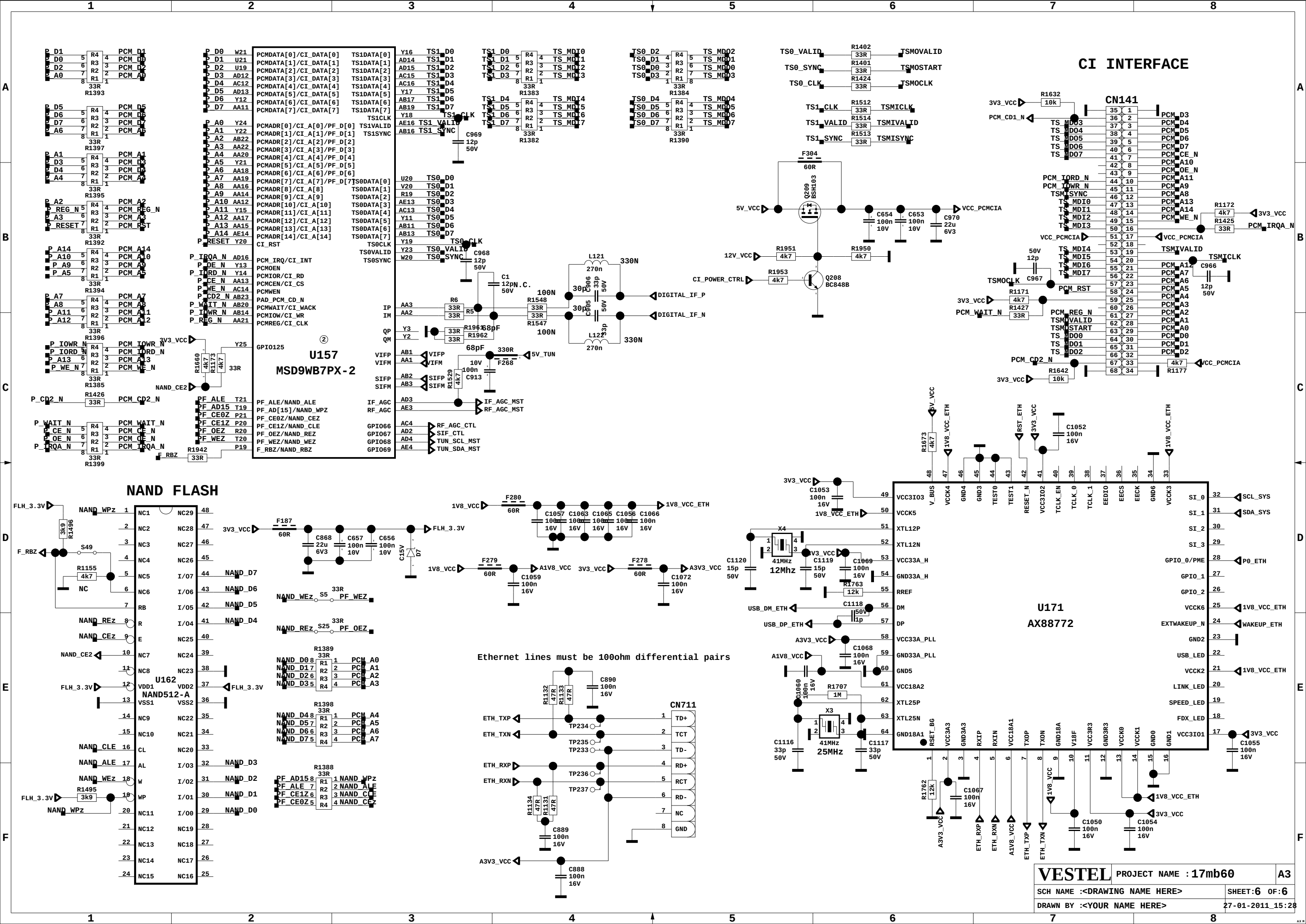


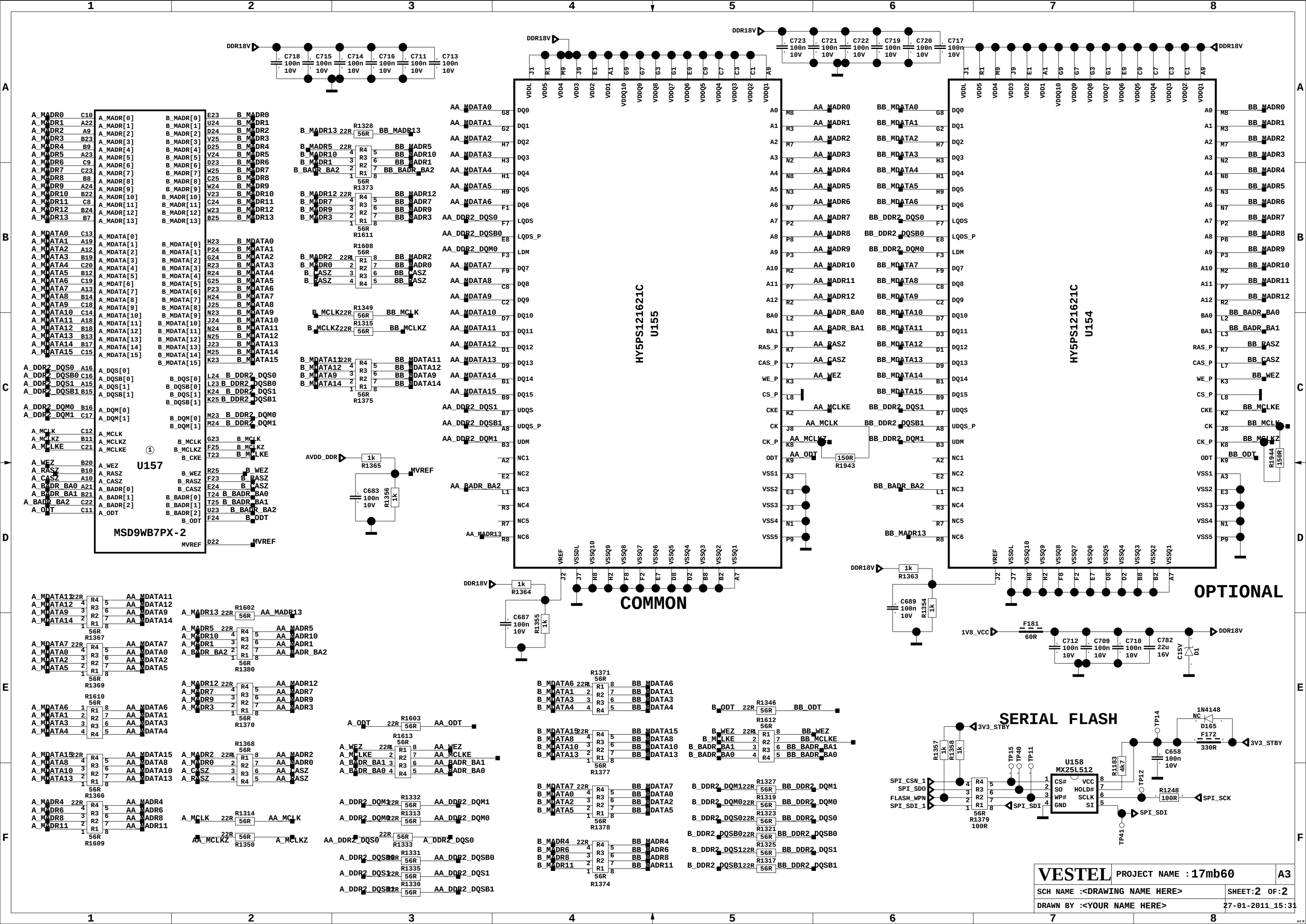
AUDIO AMP for 26" to 32"

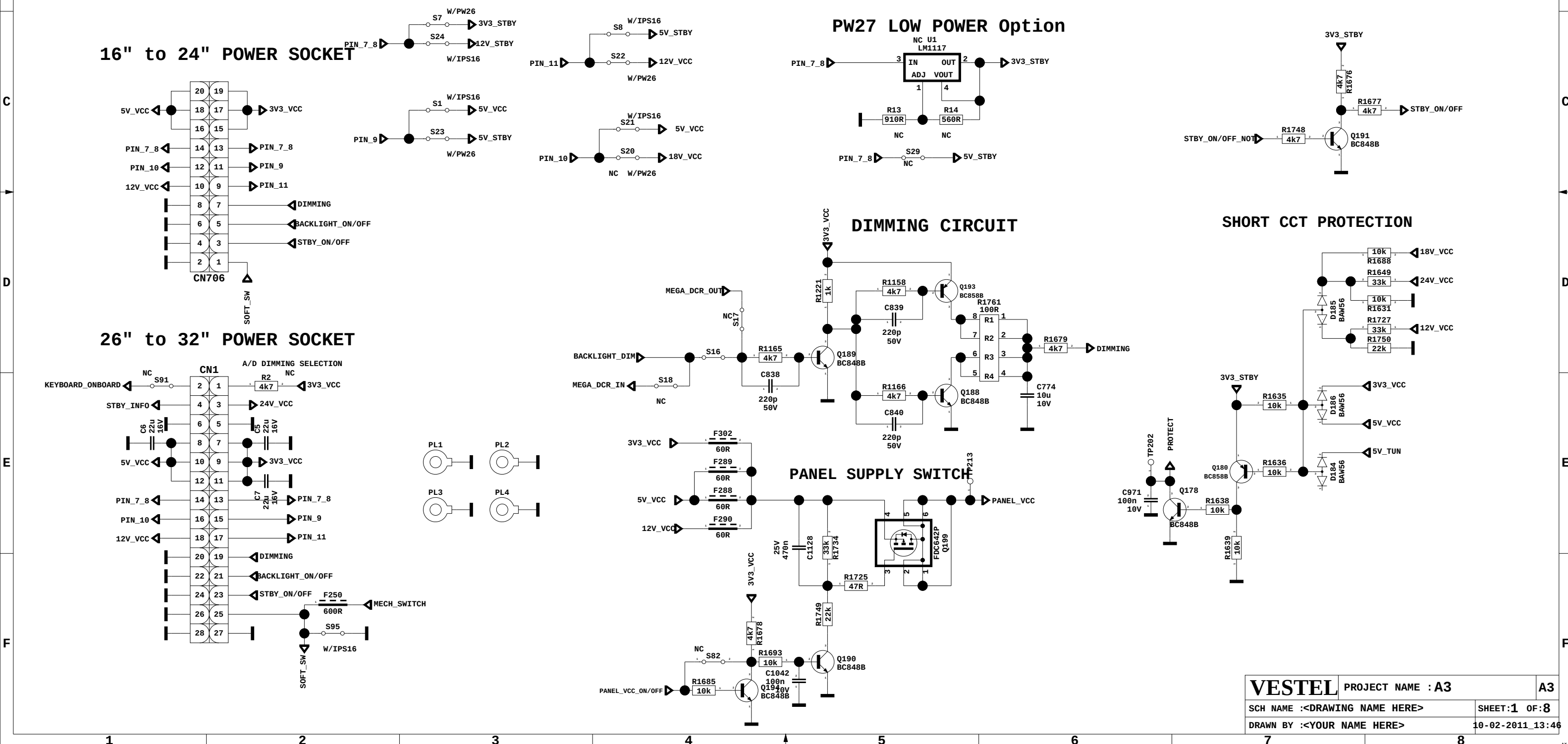


POP NOISE CIRCUIT









15.6" LED SOCKET (CN712)

- Pin 1: 12V_INV
- Pin 2: GND
- Pin 3: GND
- Pin 4: GND
- Pin 5: DIMMING
- Pin 6: BACKLIGHT_ON/OFF

CCFL INVERTER SOCKET (CN705)

- Pin 1: 12V_INV
- Pin 2: GND
- Pin 3: GND
- Pin 4: GND
- Pin 5: DIMMING
- Pin 6: BACKLIGHT_ON/OFF

The schematic diagram shows the DC/DC1 power supply and the SW2 switch circuit. The DC/DC1 section includes a 12V_STBY input, a fuse F287, and several capacitors (C1091, C1092, C1093, C1112, C1038, C1130). The core component is the U174 MP1583 converter, which is configured with feedback resistors R1737 and R1721, and a compensation network consisting of R1690, R1709, and C1130. The output of the converter is connected to a MOSFET (Q184, BC848B) through a gate resistor R1698. The MOSFET is driven by the MOSFET_CONTROL signal. The SW2 switch is connected to the MOSFET output and provides a 5V_STBY output. The SW2 switch also includes a 5V_STBY input, a 220nF capacitor (C1047), and a 47R resistor (R1722).

SW1

12V_STBY

12V_VCC

7A/32VDC

FS5

C1049 220n 25V

R1732 33k

R1724 47R

R1733 33k

R1692 10k

MOSFET_CONTROL

Q182 BC848B

FDC642P Q200

DC/DC2

12V_STBY

60R

F286

C1089 22u 16V

C1090 16V

C1114 10n 16V

U23 MP1484

BS SS 8

IN EN 7

SW COMP 6

GND FB 5

R1689 10k

C1129 10k

R1708 5k6

R3 10k

C1036 100n 18V

12V_STBY

5n6 50V

for 3v3 Panel Sup.

L123 15u

L16 10UH

C1096 22u 16V

D20 SS3

for 3v3 Panel Sup.

R1735 33k

TP244

C1097 22u 16V

3V3_STBY

SW3

3V3_STBY

3V3_VCC

7A/32VDC

FS4

C1048 220n 25V

R1731 33k

R1723 47R

R1730 33k

R1697 10k

MOSFET_CONTROL

Q183 BC848B

FDC642P Q197

The schematic diagram illustrates the MP2012 DC-DC converter circuit. The input section features a 3V3_STBY input connected to the EN pin of the MP2012. The 3V3_VCC input is connected to the FB pin. The 5V_VCC input is connected to the VIN pin. The PVIN pin is connected to the SW pin. The output section includes a 10uH inductor (L116) and a 15uF capacitor (C1101) connected to the SW pin. The output voltage is 1V8_VCC. Various resistors (R1738, R1769, R1770, R1792) and capacitors (C1107, C1101, C1098, C1094, C1093) are shown with their values and tolerances.

The schematic diagram illustrates the MP1484 DC-DC converter circuit. The input stage features a 12V_VCC supply connected to the IN pin (pin 2) through a 60R resistor and a 22uF capacitor (C1085). The SW pin (pin 3) is connected to the output of the input filter, which includes a 22uF capacitor (C1084) and a 16V zener diode (F273). The GND pin (pin 4) is connected to the common ground. The feedback network consists of a 10k resistor (R1768) connected to the FB pin (pin 5) and a 10k resistor (R1769) connected to the EN pin (pin 7). The output stage includes a 10uH inductor (L117) connected to the SW pin (pin 3) and a 22uF capacitor (C1087) connected to the output. The output is filtered by a 22uF capacitor (C1086) and a 16V zener diode (F273). The circuit is powered by 12V_VCC and 1V2_VCC.